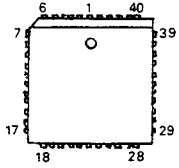
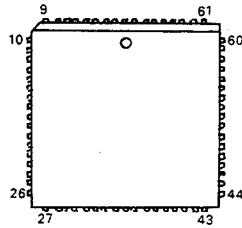


**SOLID STATE DEVICE BASE DIAGRAM
GRUNDDIAGRAM DER FESTKÖRPEREINRICHTUNG**

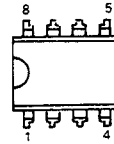
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RH-IX1406BMZZ
RH-IX1407BMZZ
RH-IX1423BMZZ



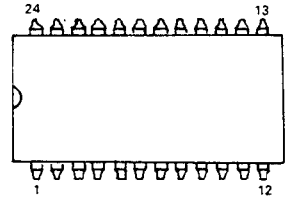
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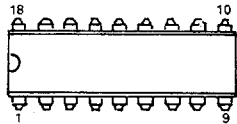
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RH-IX1420BMZZ



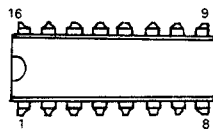
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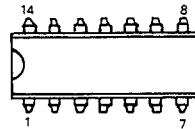
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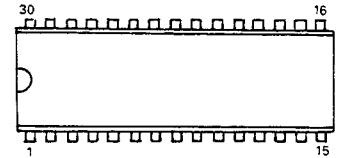
RH-IX1414BMZZ
RH-IX1410BMZZ



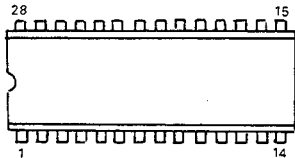
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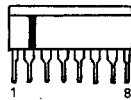
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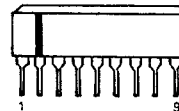
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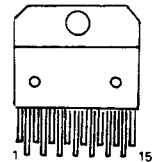
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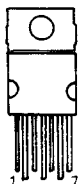
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RH-IX1416BMZZ



RH-IX1413BMZZ



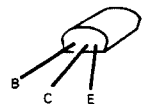
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RH-IX1185BMZZ



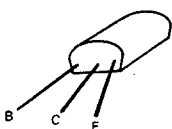
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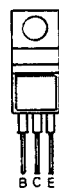
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VS2SC18156W-1
VS2SC1906111E



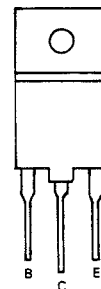
VS2SC2271-D1A



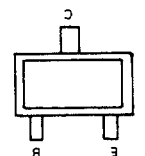
VS2SD1913511E
VS2SD1913XX1E



VS2SD1546XX1E



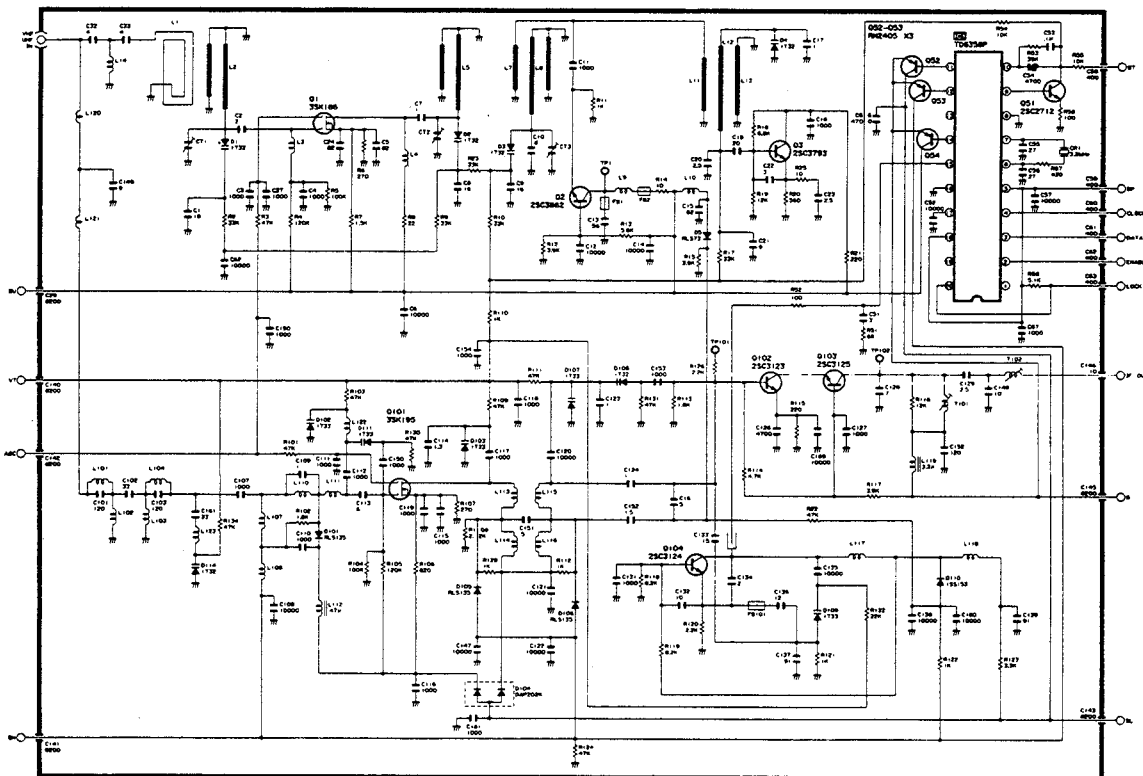
VS2SA1037KQ-1
VS2SC2412KQ-1



(SMD COMPONENT)

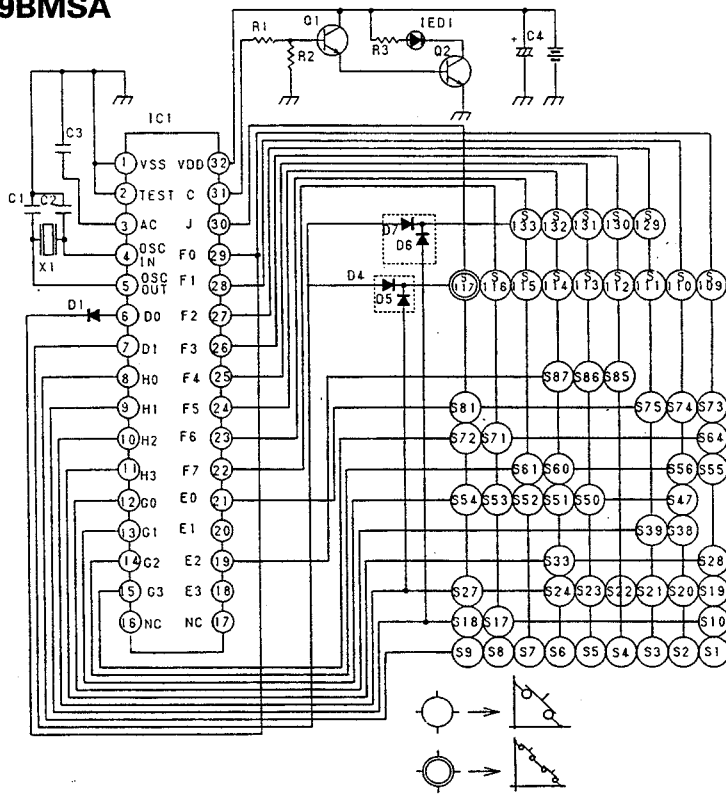
TUNER

VTUVTSA 1 SPL //



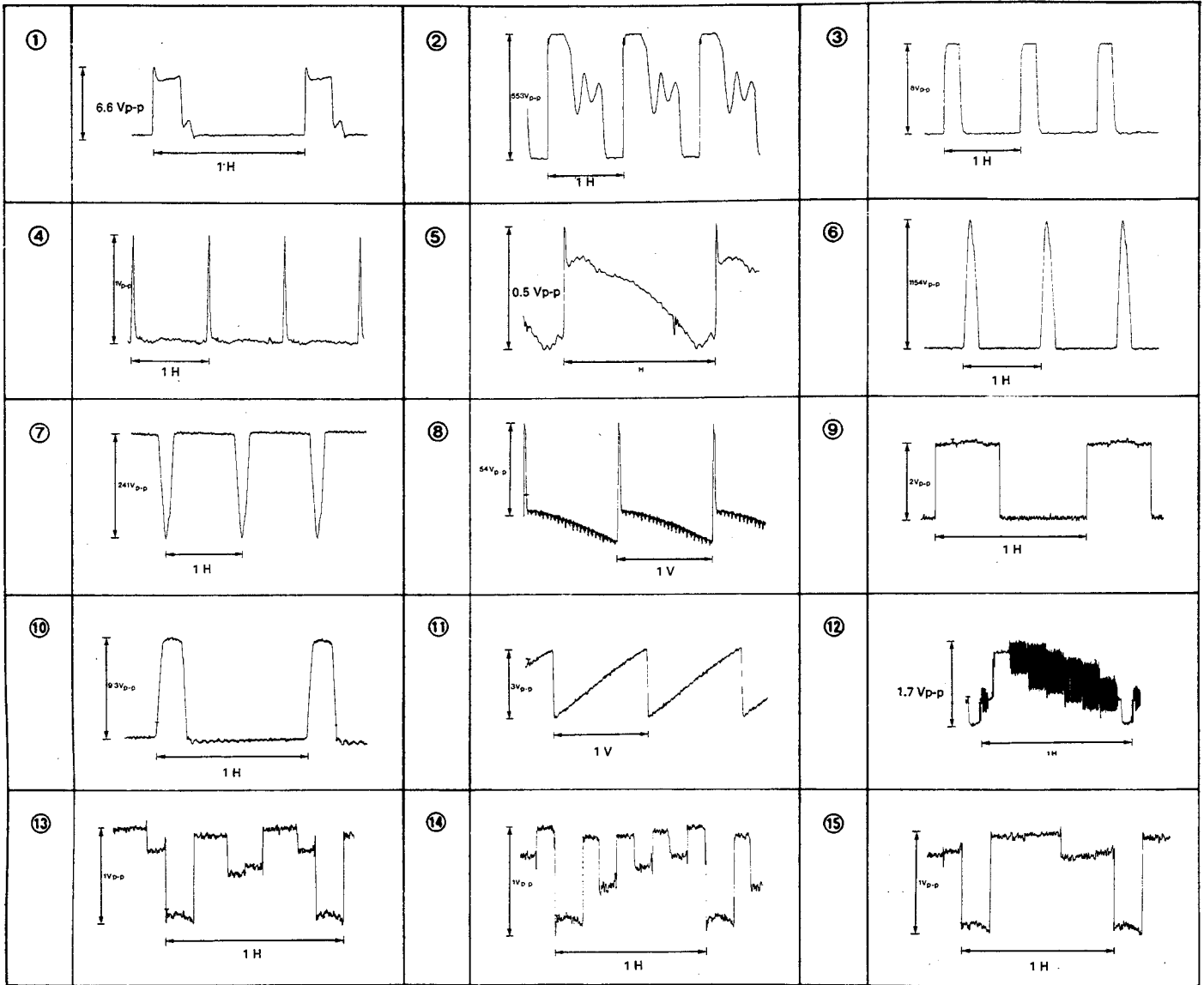
INFRARED REMOTE CONTROL UNIT SCHEMATIC DIAGRAM
 INFRAROTFERNBEDIENUNGSEINHEIT SCHEMATISCHER SCHALTPLAN

RRMCG0739BMSA



D1, (M5) (D6.7)	DAN202K
IED1	GL-521
X1	CSB455EBL
Q2	2SC2411KT97
Q1	2SC2412K
R3	1.5Ω (1/4W)
R2	22KΩ (1/10W)
R1	2.2KΩ (1/10W)
C4	47µF 6.3V
C3	0.1µF
C1.2	100pF
IC1	M50461-056FP (iX0733PA)

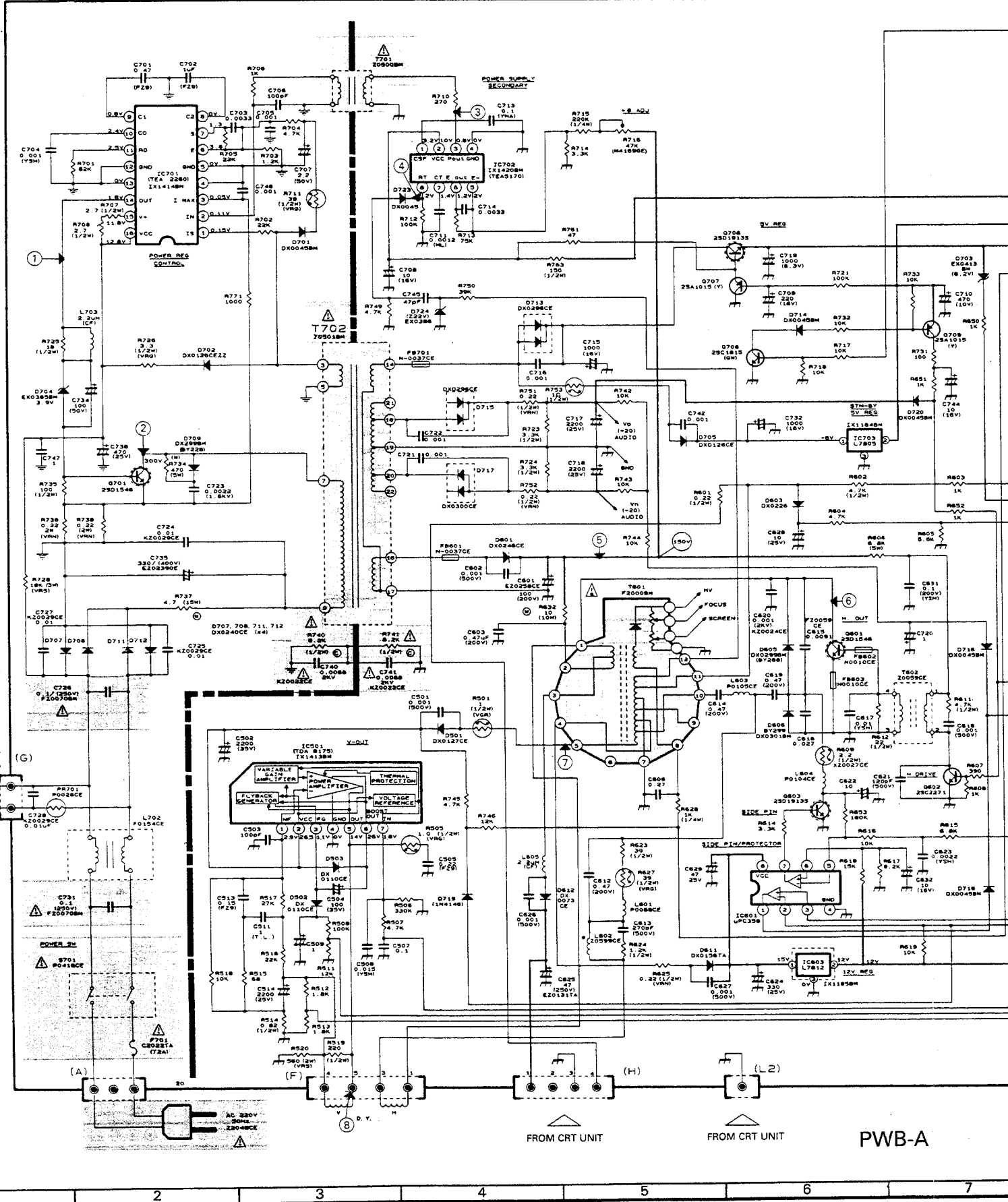
WAVEFORMS / SIGNALFORMEN



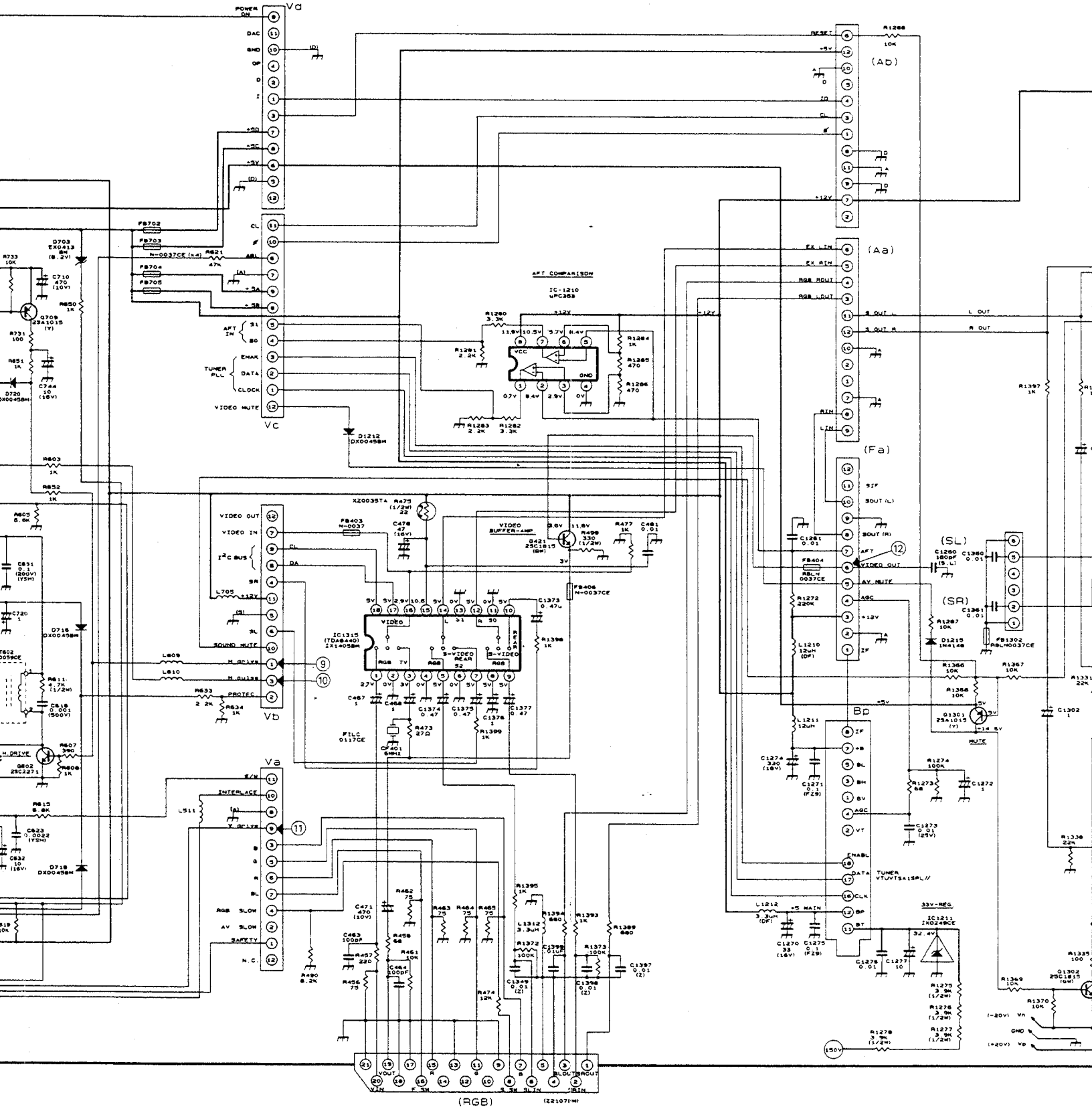
NOTE:
 WAVEFORMS N°S 1 to 12 ARE SHOWN ON MOTHER UNIT (PWB-A) DIAGRAM.
 WAVEFORMS N°S 13 to 15 ARE SHOWN ON VIDEO UNIT (PWB-B) DIAGRAM.

H
G
F
E
D
C
B
A

SCHEMATIC DIAGRAM MOTHER UNIT

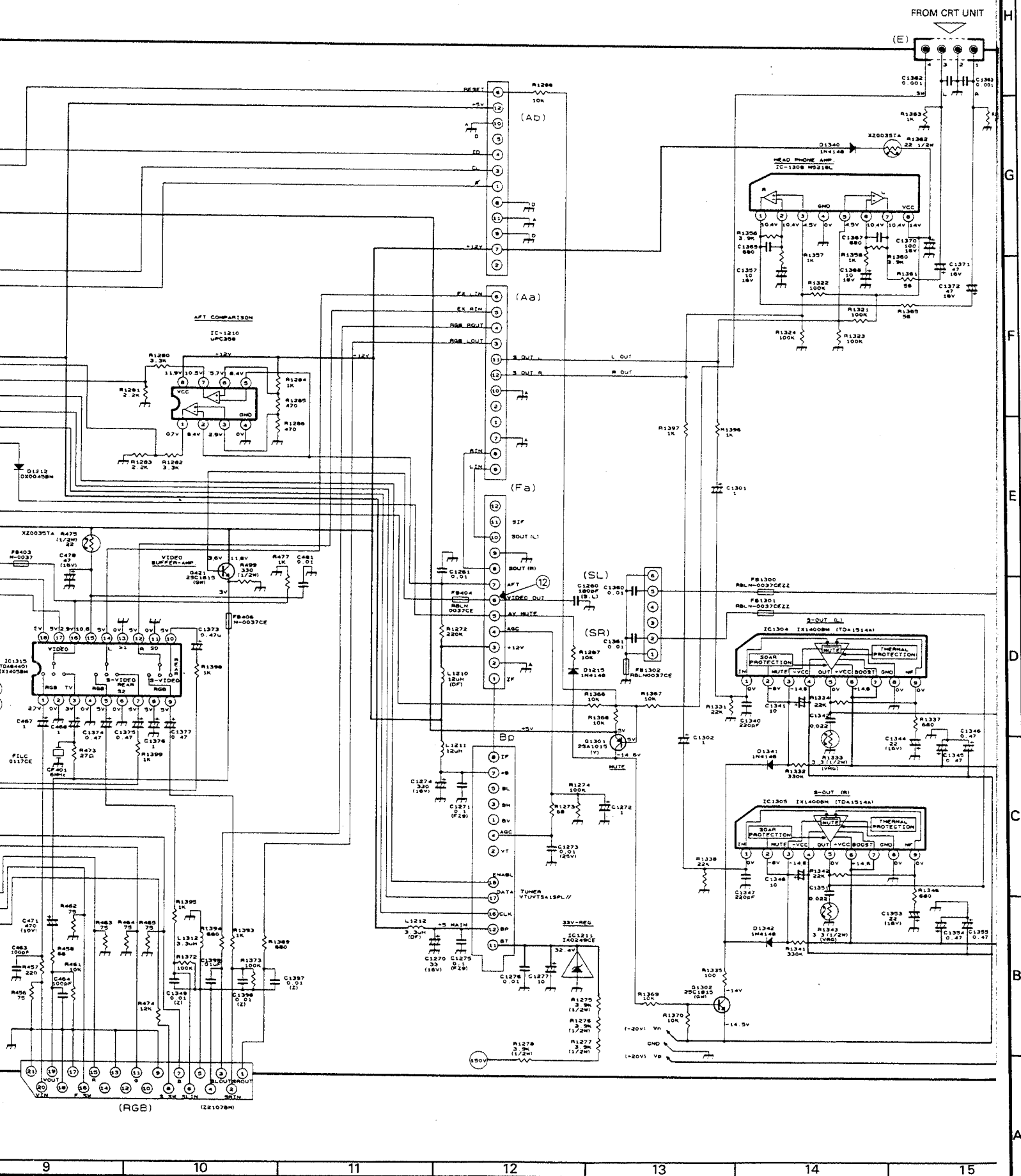


SCHEMATISCHER SCHALTPLAN HAUPTPLATINE

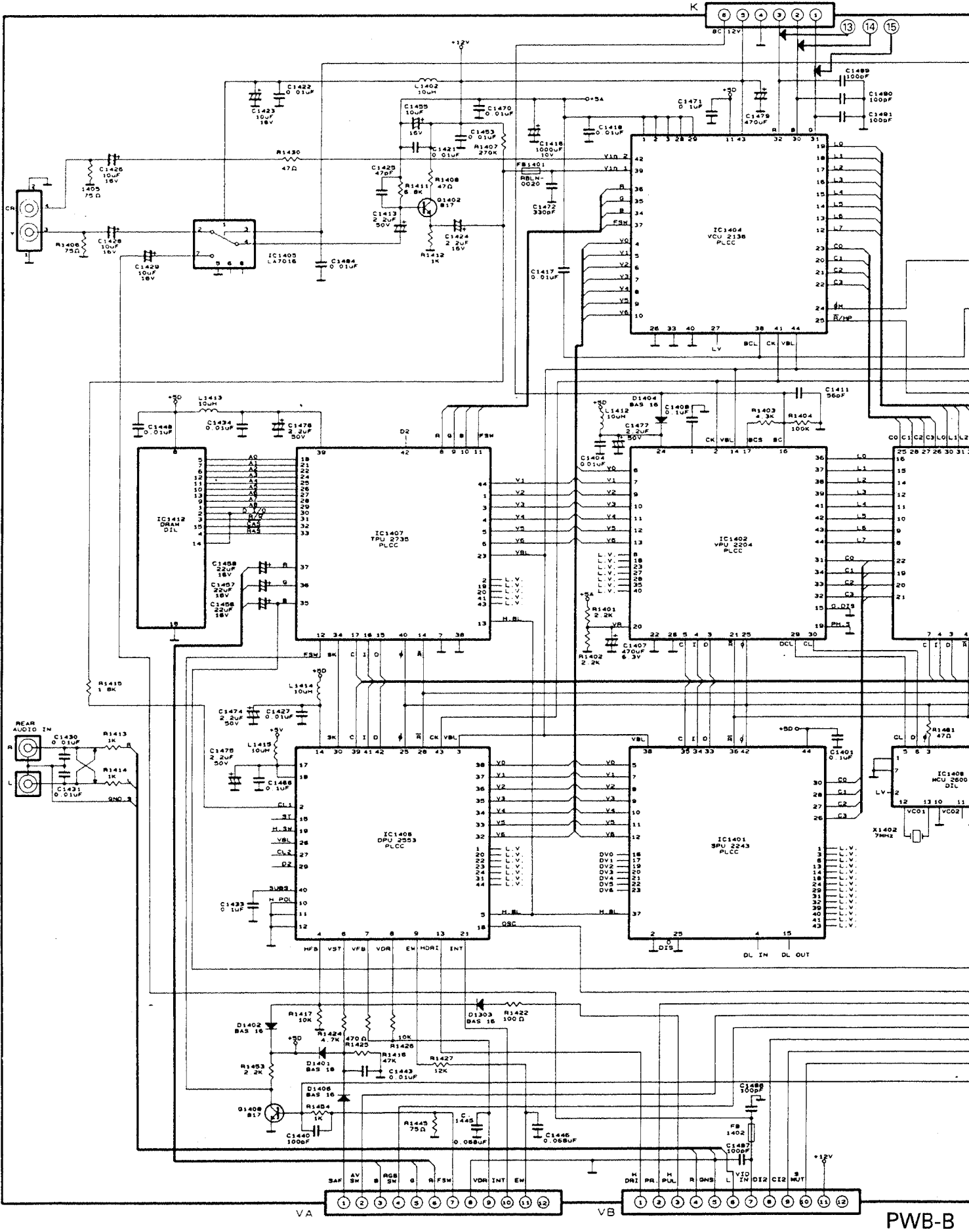


-A

PLAN HAUPTPLATINE

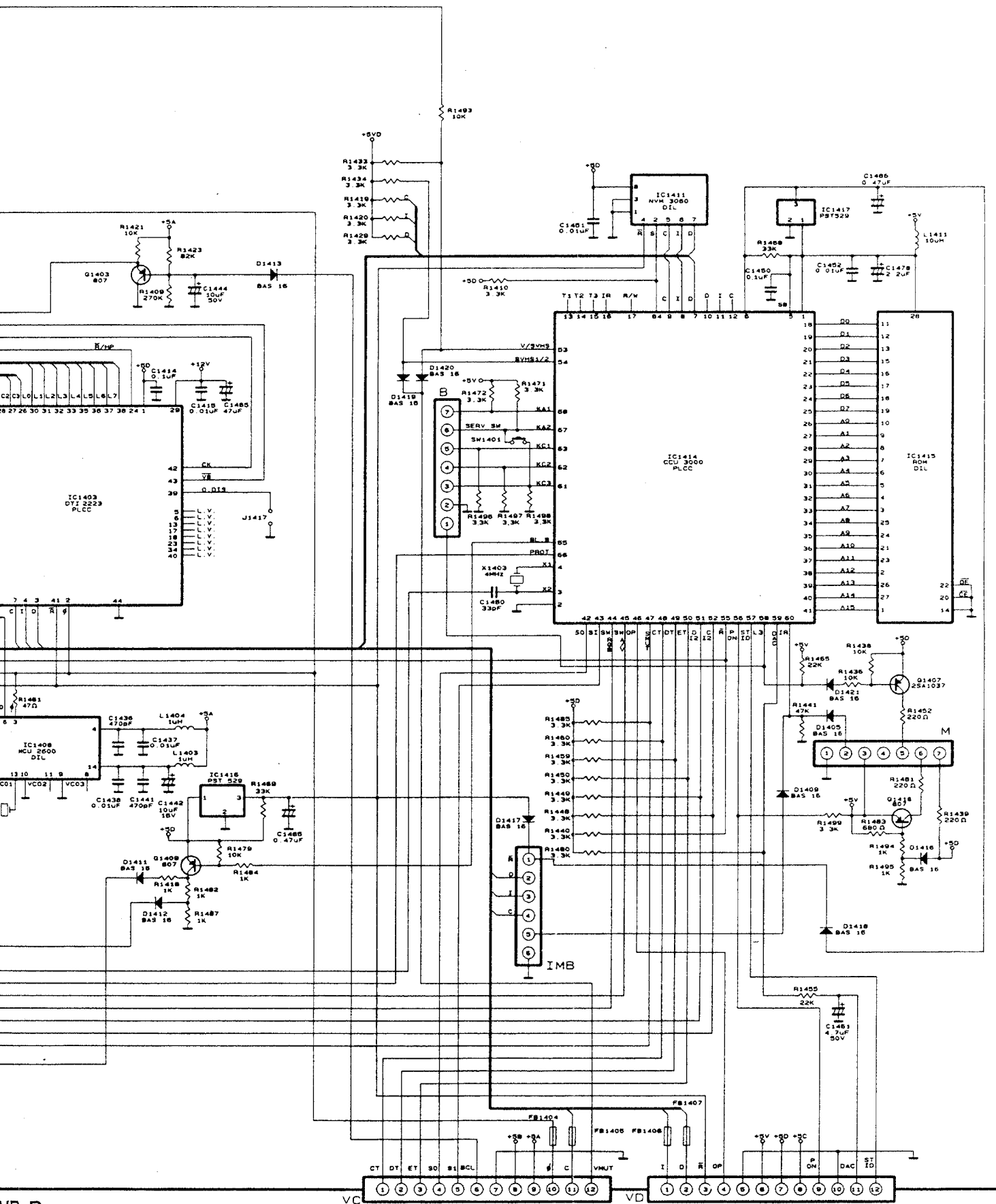


SCHEMATIC DIAGRAM VIDEO UNIT



PWB-B

SCHEMATISCHER SCHALTPLAN VIDEO-EINHEIT



VB-B

7

8

9

10

11

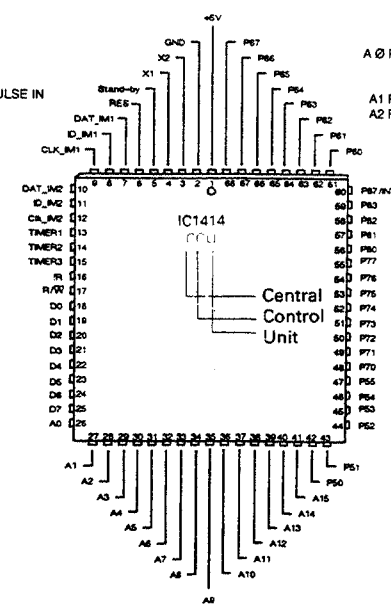
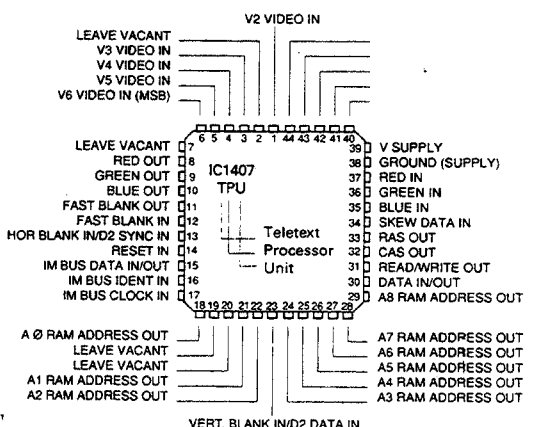
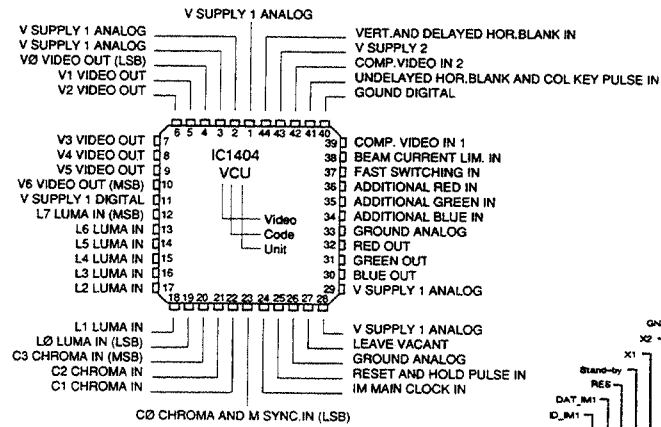
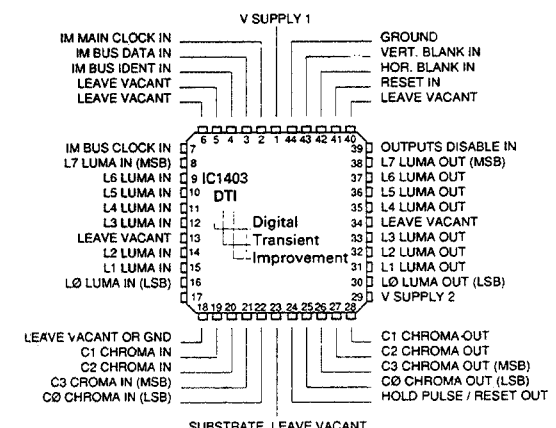
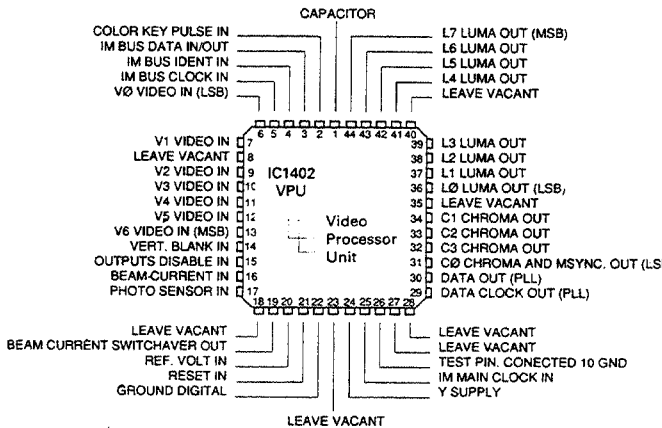
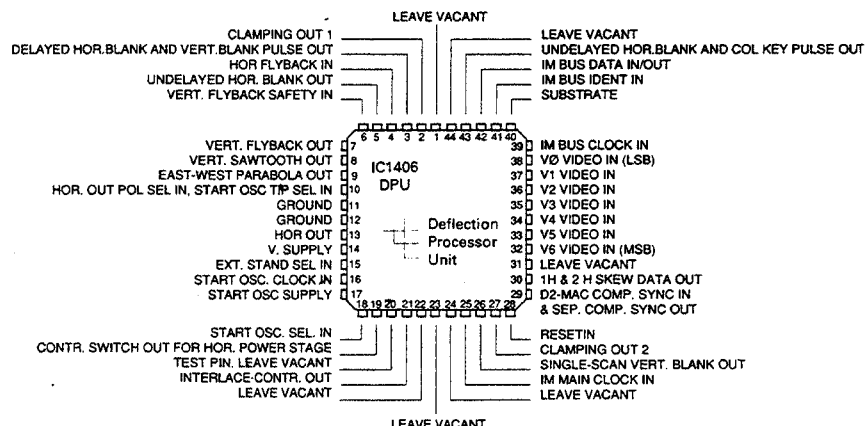
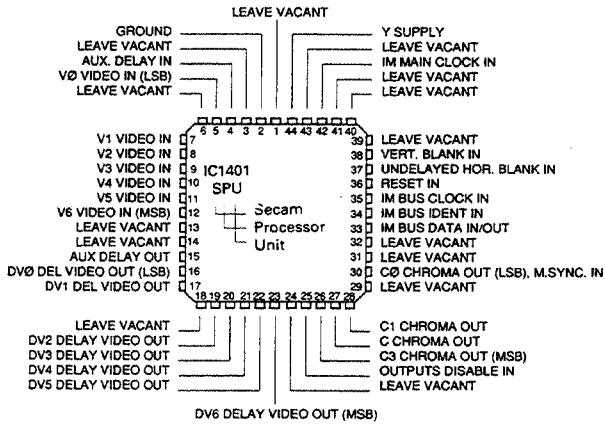
12

ABBREVIATIONS AS SHOWN ON THE VIDEO UNIT SCHEMATIC DIAGRAM

VA1 SAF	= SAFETY. Picture tube protection against burning in case of malfunction of vertical deflection.
VA2 AV SW	= AV, switching.
VA3 B	= Blue.
VA4 RGB SW	= RGB, switching. VA4
VA5 G	= Green.
VA6 R	= Red.
VA7 FSW	= Fast Switching (fast blanking input).
VA8 GNA	= Analog ground.
VA9 VDR	= Vertical drive.
VA10 INT	= Interface control output: vertical stage control in non interlace mode.
VA11 EW	= East/West parabola output.
VB1 H.DRI	= Horizontal driver output.
VB2 PR	= CCU input protection-functional blocking.
VB3 H PUL	= Horizontal pulse.
VB4 R	= Right (Audio channel).
VB5 GNS	= Ground Sound.
VB6 L	= Left (Audio channel).
VB7 VID IN	= Video input.
VB8 DI 2	= I2C Data.
VB9 CI 2	= I2C Clock.
VB10 S MUT	= Sound Mute.
VB11	= + 12 v.
VC1 CT	= Clock Tuner.
VC2 DT	= Data Tuner.
VC3 ET	= Enable Tuner.
VC4 S0	= Tuner variables.
VC5 S1	= Tuner variables.
VC6 BCL	= Beam current limiter (ABL).
VC7 GNA	= Analog ground.
VC8 + 5B	= 5V.
VC9 + 5A	= 5V analog.
VC10	= Main clock, generated by MCU.
VC11 C	= IMBUS clock (IMC).
VC12 VMUT	= Video Mute.
VD1 I	= IMI (IMBUS identification).
VD2 D	= IMD (IMBUS data).
VD3 R	= Reset (low level function).
VD4 OP	= OPTION (not used).
VD5 GND	= Ground digital.
VD6 + 5V	= 5 Volts standby.
VD7 + 5D	= 5 Volts digital.
VD8 + 5C	= 5 Volts clock.
VD9 P ON	= Power on.
VD11 DAC	= D/A converter Audio Control (not being controlled by IMBUS).
V0..V8	= Digitalized Video Signals.
L0..L7	= Digitalized Luminance Signals.
C0..C3	= Digitalized Chrominance Signals.
DV0..DV7	= Delayed digitalized Video Signals.
BL.B	= Blue back.
V/SVHS	= Switching Video to SVHS.
SVHS1/2	= Switching SVHS1 to SVHS2 (2 possible inputs).
KA1, KA2	= Keyboard Filters.
KC1, KC2, KC3	= Keyboard columns.
D0..D7	= Memory data signals.
A0..A15	= Memory address signals.
O DIS	= Output disable.

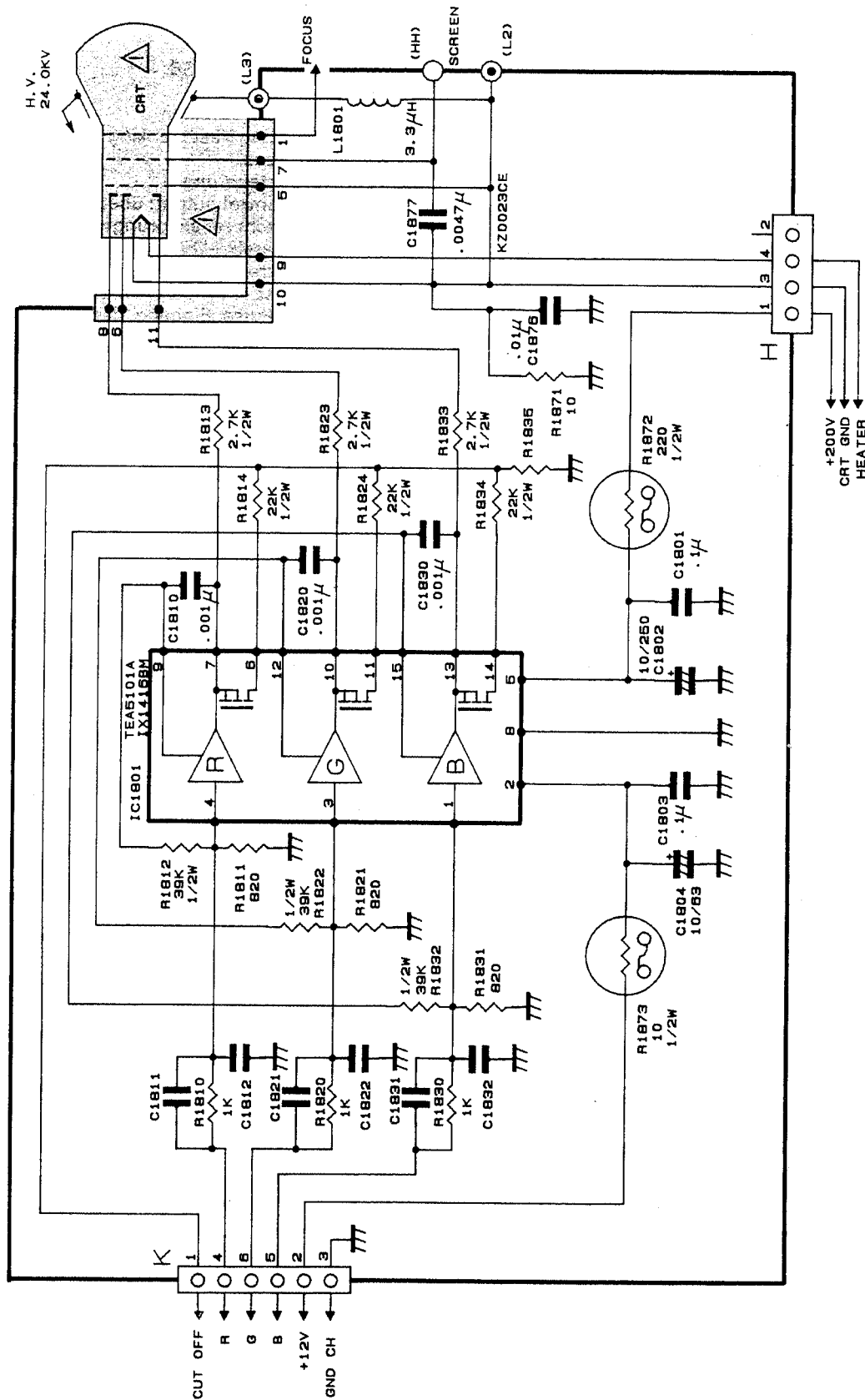
EINIGE ABKÜRZUNGEN DIE AUF DEM VIDEOMODUL ERSCHEINEN

SAFETY. Schützt die Bildröhre vor dem Durchbrennen im Fall ein Fehlfunktion der vertikalen Ablenkung.
Umschaltung auf AV.
Blau.
Umschaltung auf RGB.
Grün.
Rot.
Fast Switching (fast blanking input).
Analog Masse.
Vertikal ansteuerung.
Interface control output: überprüft die vertikale Ablenkung für den "Nicht zeilensprungverfahren» Betrieb.
Ost/West Parabel-Output.
Horizontal treiberausgang.
Input-Schutz - Blockiert Funktion.
Horizontal Pulse. Eingang, der von der DPU benötigt wird, durch welchen die Flyback-Impulse über die Festigung (Diode) einlaufen.
Rechts (Rechter Audio-Kanal).
Masse Ton.
Links (Linker Audio-Kanal).
(Video-Eingang, der aus einer externen Quelle stammt).
Daten des I2C
Takt des I2C.
Top still (Sound Mute).
+ 12 V.
Tackreinstellung.
Datentuner.
Enable Tuner.
Tuner-Variablen.
Tuner-Variablen.
Beam current limiter (Strahlstrombegrenzung, ABL).
Analog Masse.
5V.
5V analog.
Haupt-Takt, betrieben durch den MCU des Kathodenstrahls ABL.
Takt des IMBUS (IMC).
Video Mute.
IMI (IMBUS - Identifikation).
IMD (IMBUS Daten).
Reset (funktioniert auf niedrigem Niveau).
Option (wird nicht belegt).
Digitaler Masse.
5 Volt Standby.
5 Volt digital.
5 Volt Takt.
Power on.
Digital-Analog-Wandler für Audio-Kontrolle, der nicht durch IMBUS zu kontrollieren ist.
Digitalisierte Video-Signale.
Digitalisierte Leuchtdichtesignale.
Digitalisierte Chrominanz-Signale
Verzögerte digitalisierte Video-Signale
Blue back.
Umschalter von Video auf SVHS.
Umschalter von SVHS1 auf SVHS2 (zwei mögliche Eingänge)
Keyboard-Filter.
Spalten für den Speicher
Datensignale für den Speicher.
Richtungssignale (ADDRESS) für Speicher.
Disable ausgang.



CRT UNIT

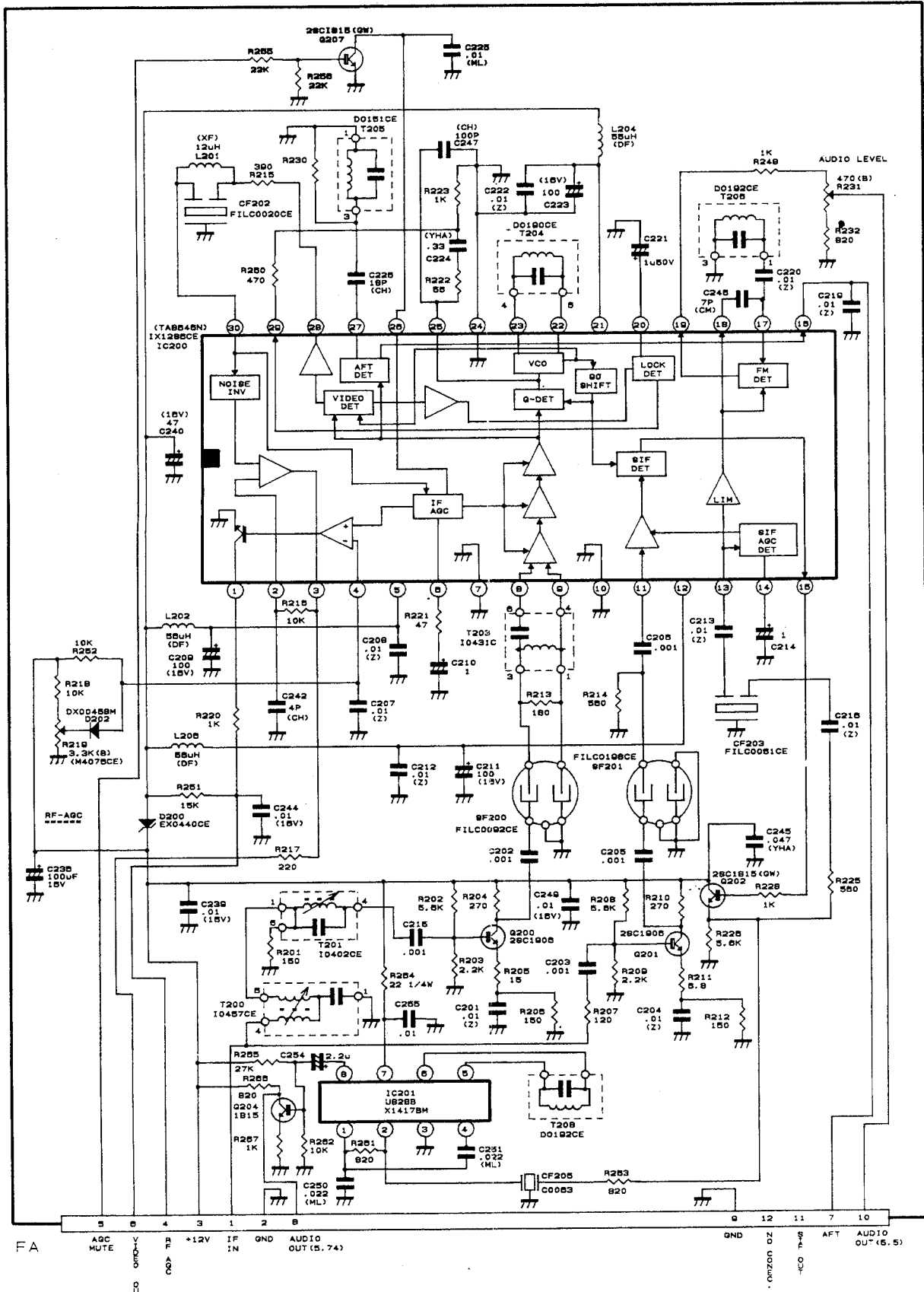
BILDRÖHRENPLATINE



PWB-C

IF UNIT

ZF-EINHEIT

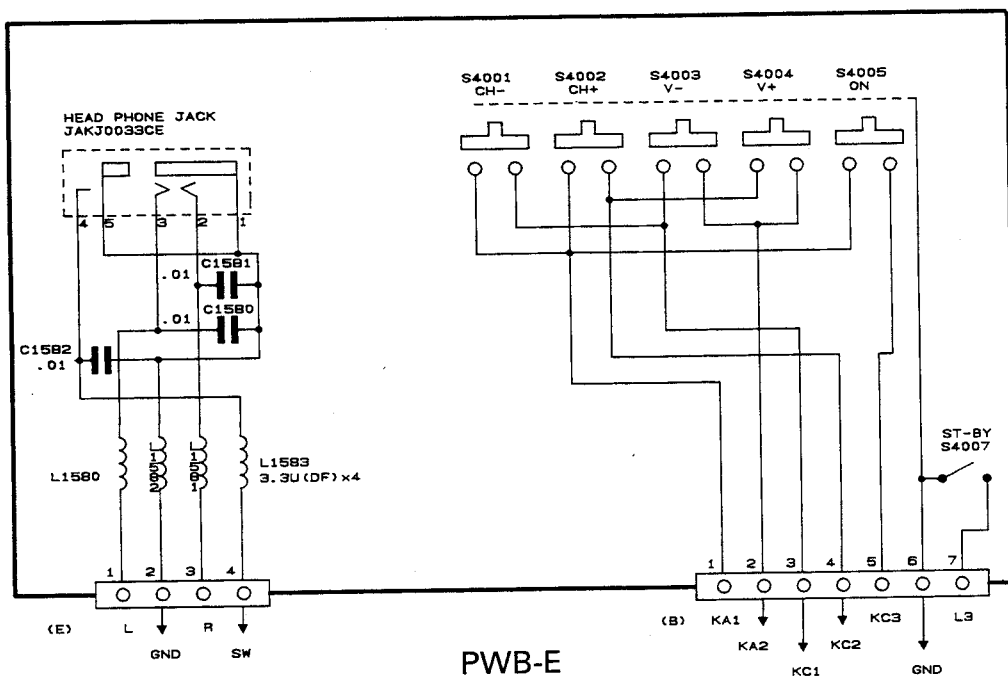


PWB-D

7	8	9	10	11	12
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CONTROL UNIT

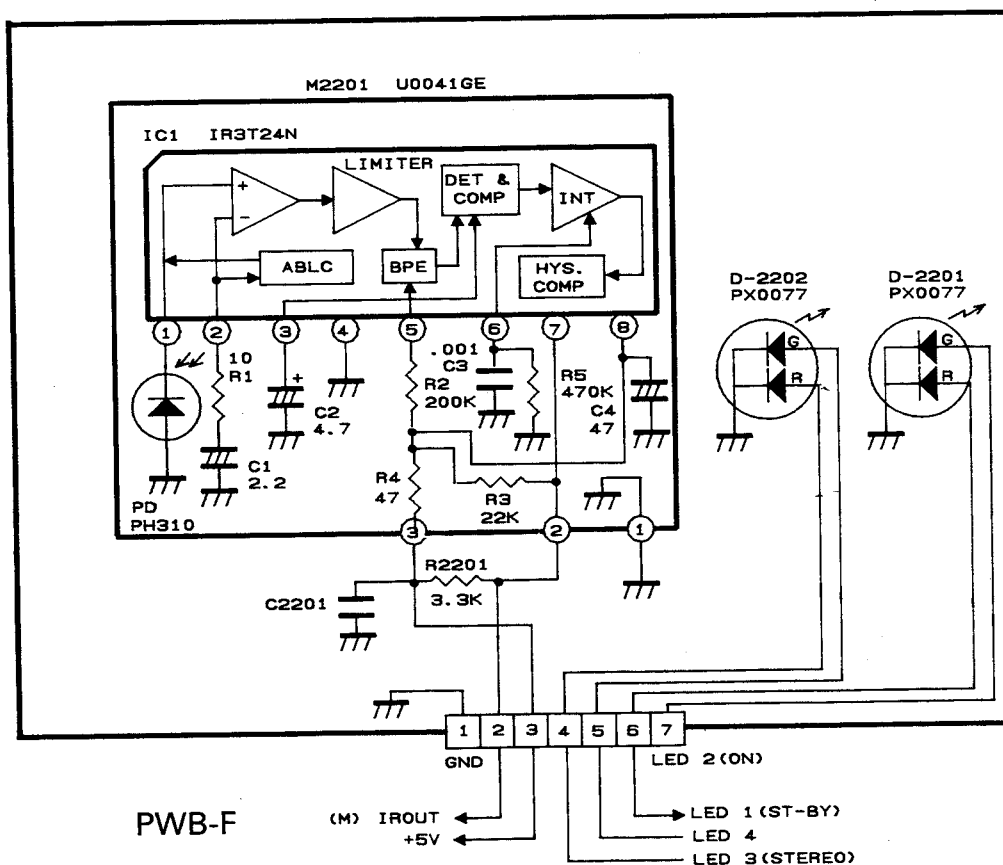
REGLER-EINHEIT



PWB-E

IR UNIT

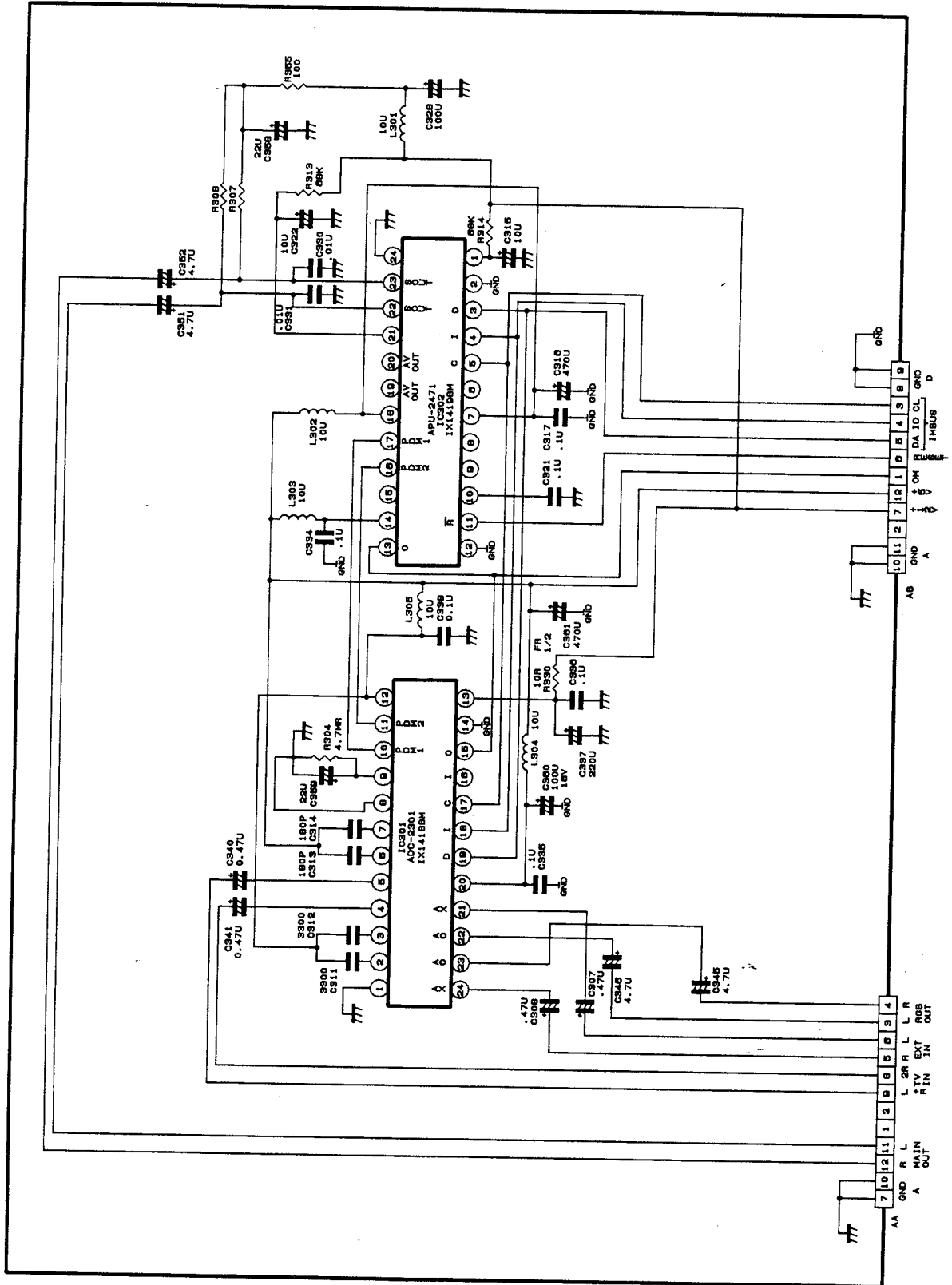
IR-EINHEIT



PWB-F

IGR AUDIO UNIT

AUDIO-EINHEIT



PWB-G

BLOCK DIAGRAM MOTHER BOARD

